

FIG. 1

貴社No. F008769

幣所No. EP9002

SE-0241

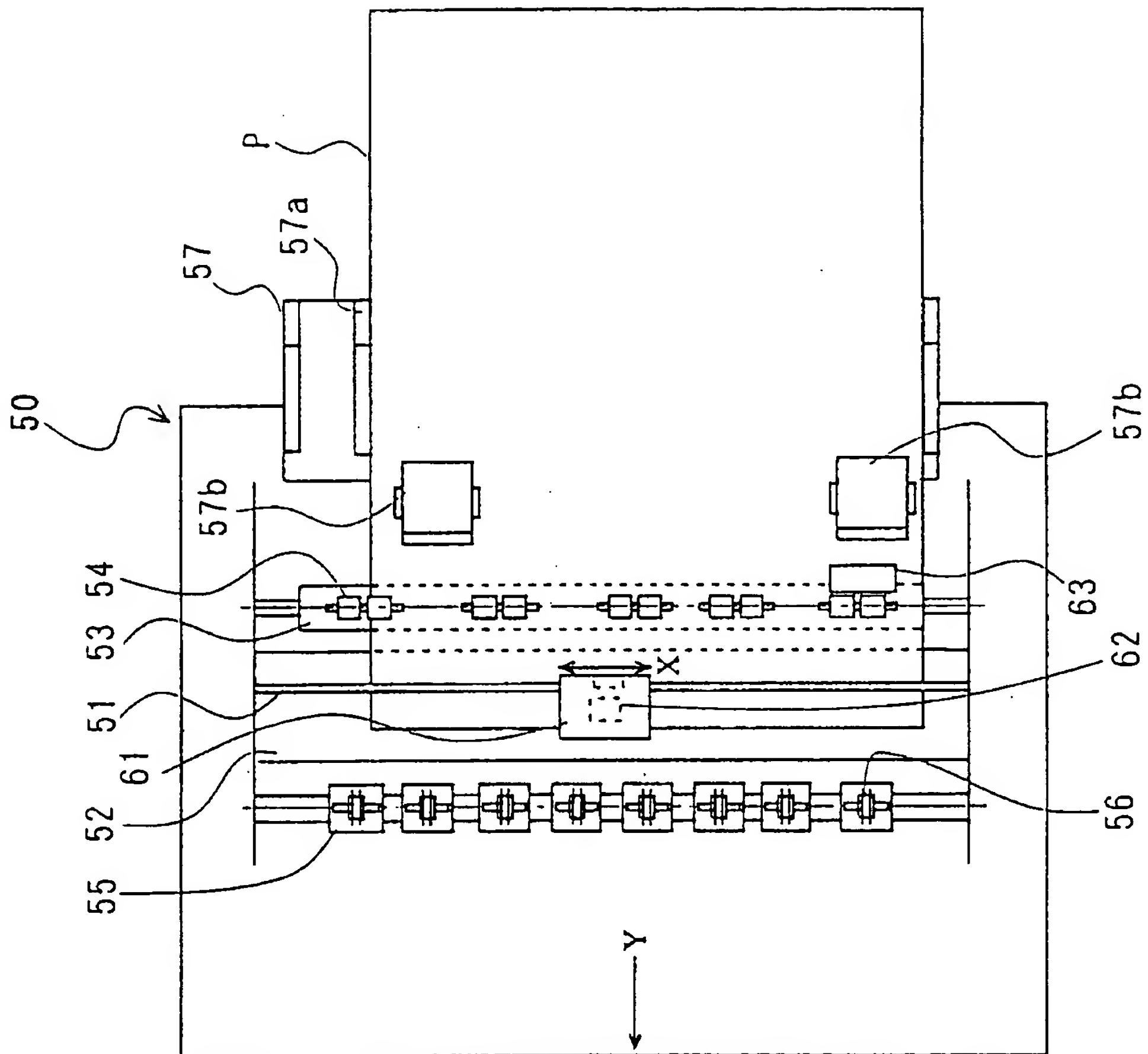
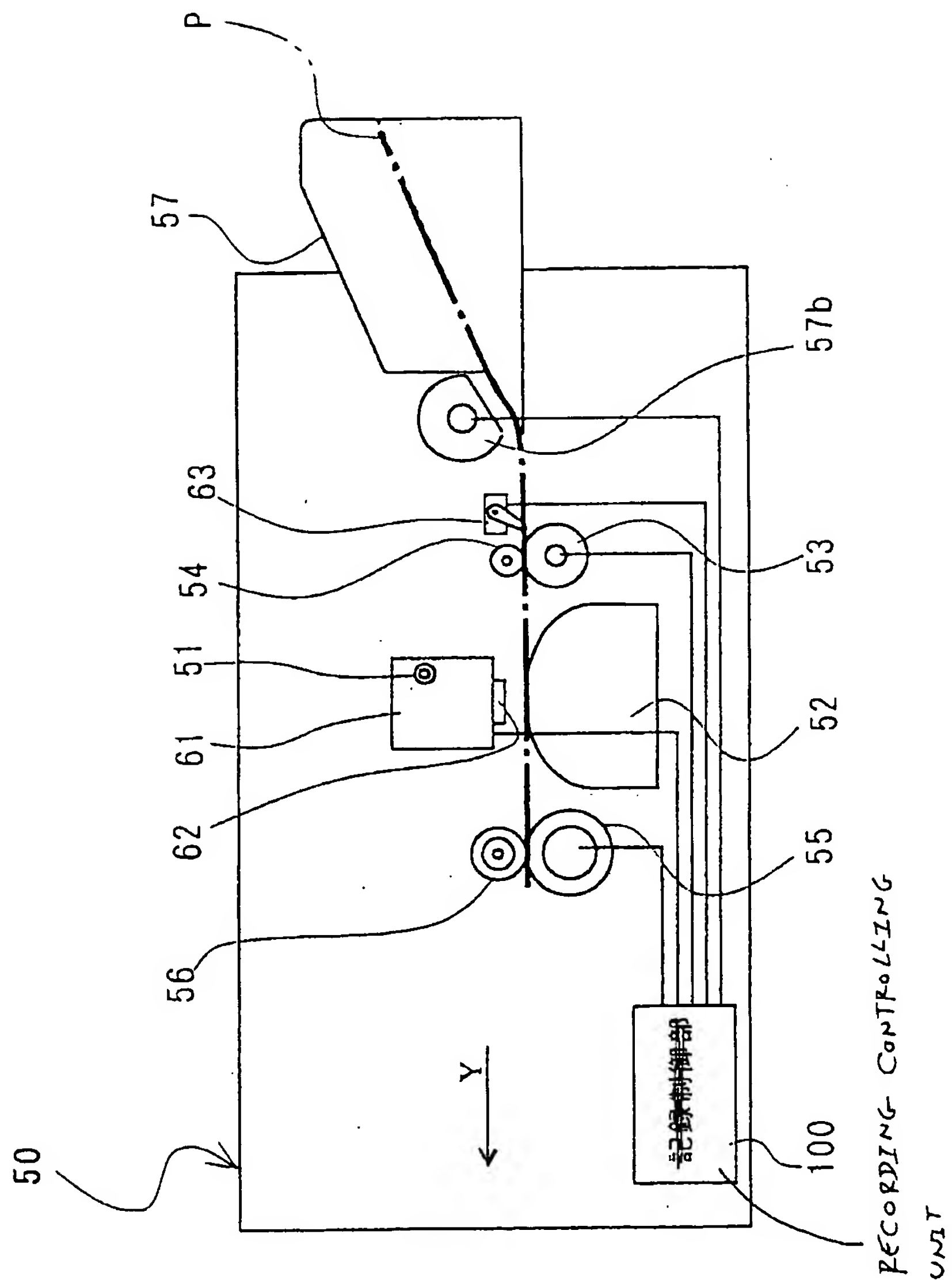


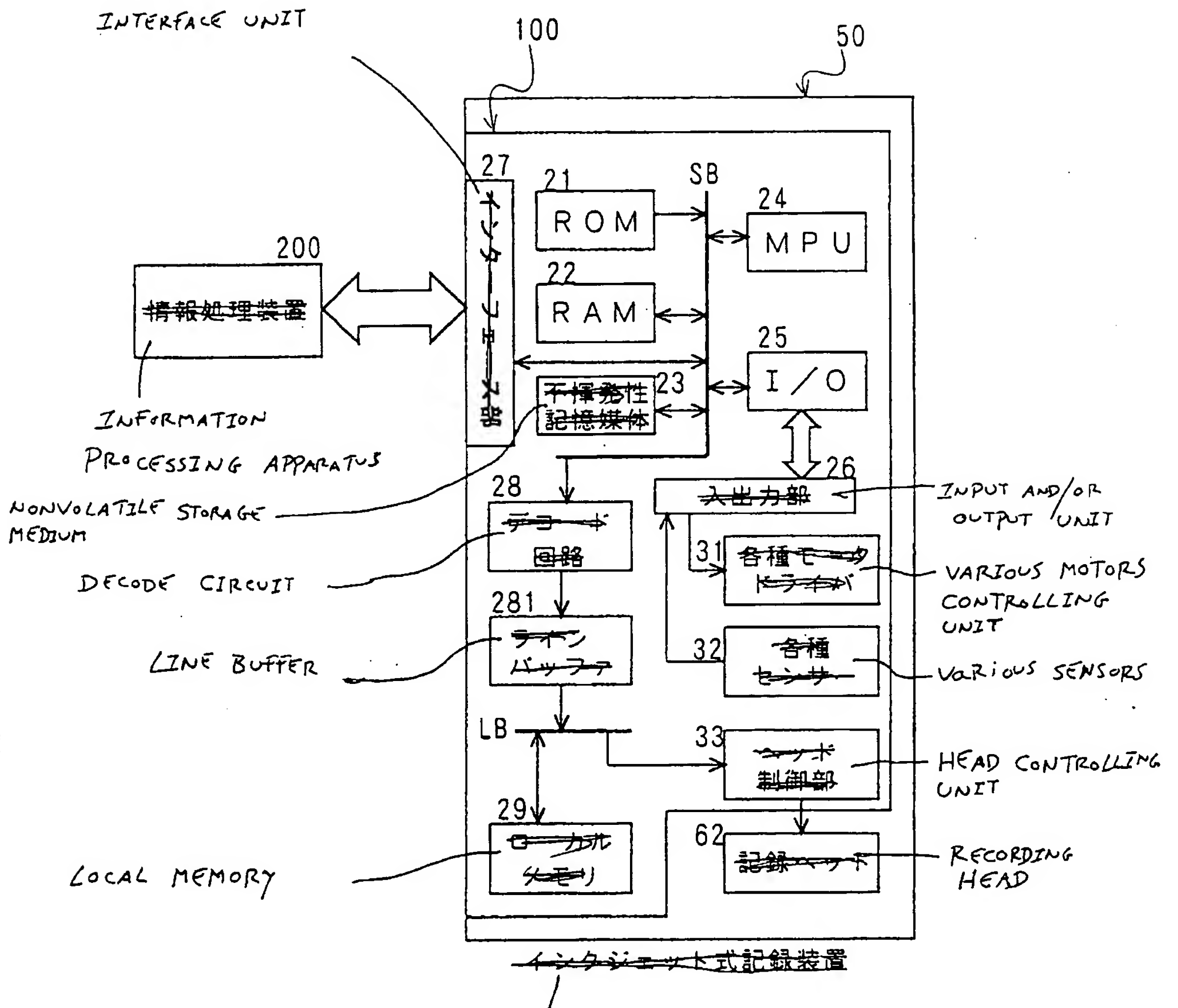
FIG. 2

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INKJET TYPE RECORDING APPARATUS



The diagram illustrates the architecture of a recording system. At the top, three main components are shown: ROM (21), MPU (24), and RAM (22). These are connected to a central system bus (SB). Below the bus, a dashed box labeled 'HEAD CONTROLLING UNIT' (10) contains an ASIC (4). The ASIC is composed of several sub-units: an input section (27) receiving data from an '情報処理装置' (Information Processing Apparatus, 200); a control section (33) connected to the input section via a bus (IB1); a decoder section (41) connected to the control section via a bus (IB2); and a recording section (62) connected to the decoder section via a bus (IB3). The recording section is connected to a 'RECORDING HEAD'. In the foreground, an 'INTERFACE UNIT' (29) is connected to the system bus (SB) and a 'RECEIVING BUFFER UNIT'. The entire system is connected to a 'LOCAL MEMORY'.

FIG. 5

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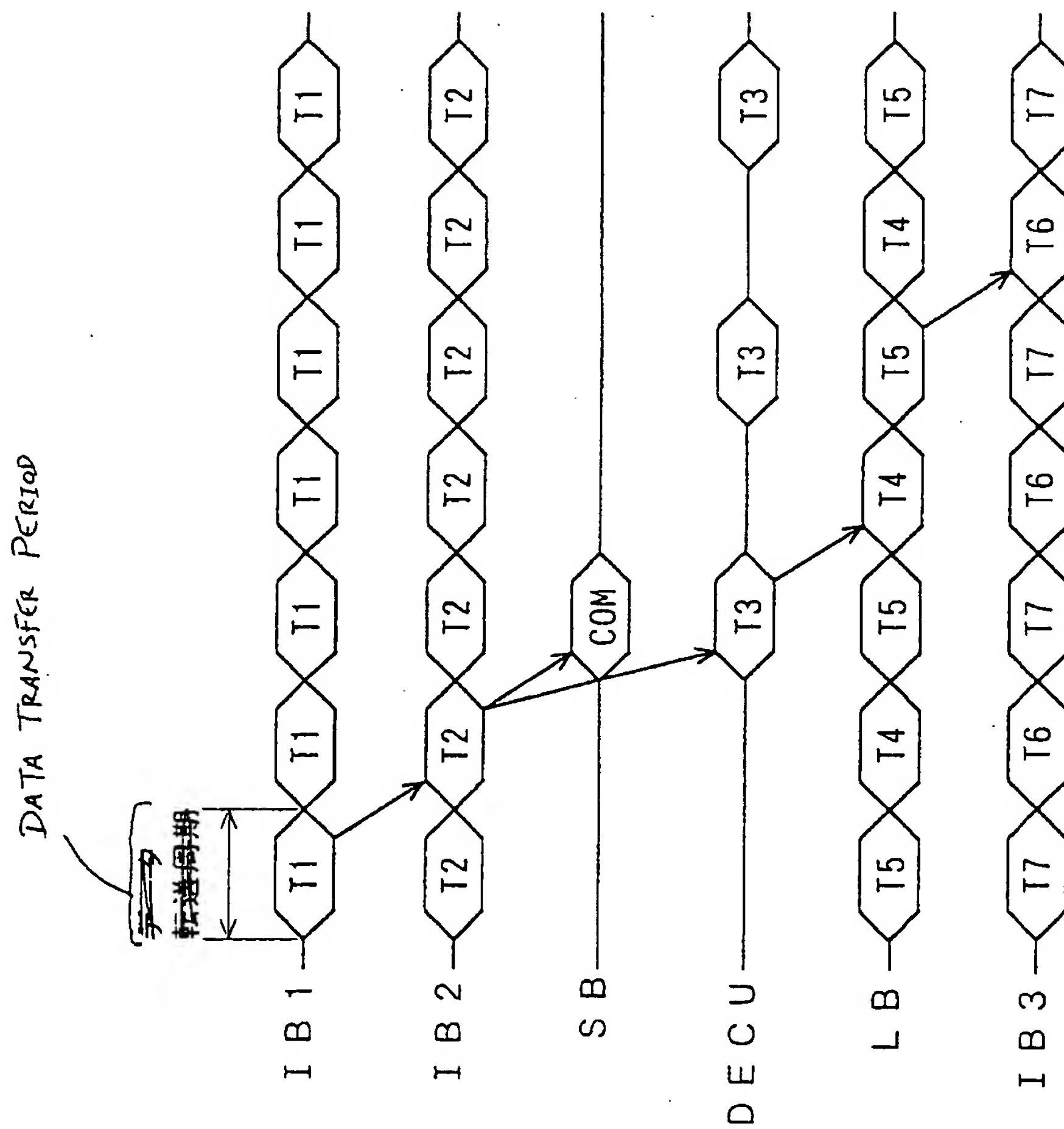


FIG. 6

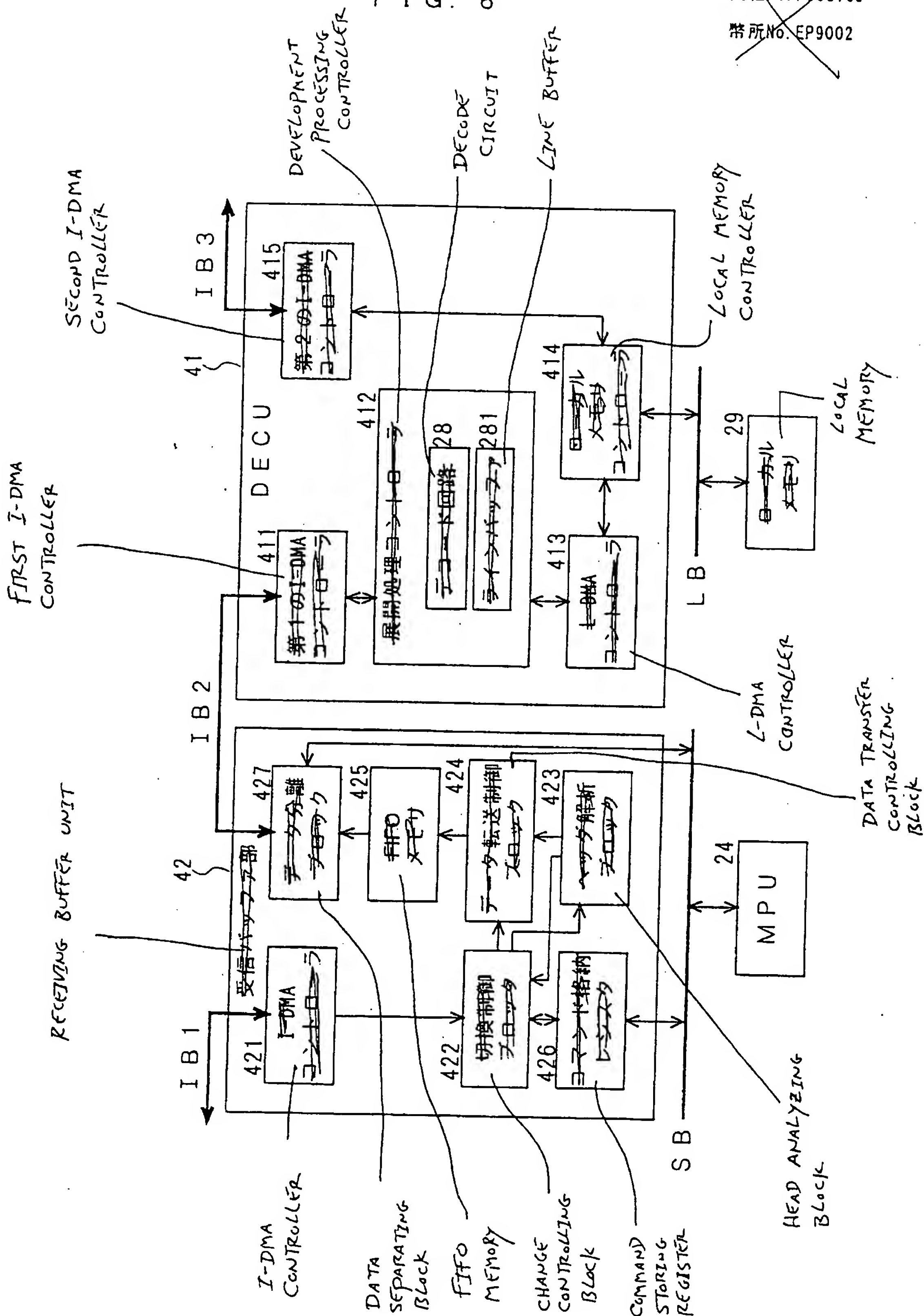
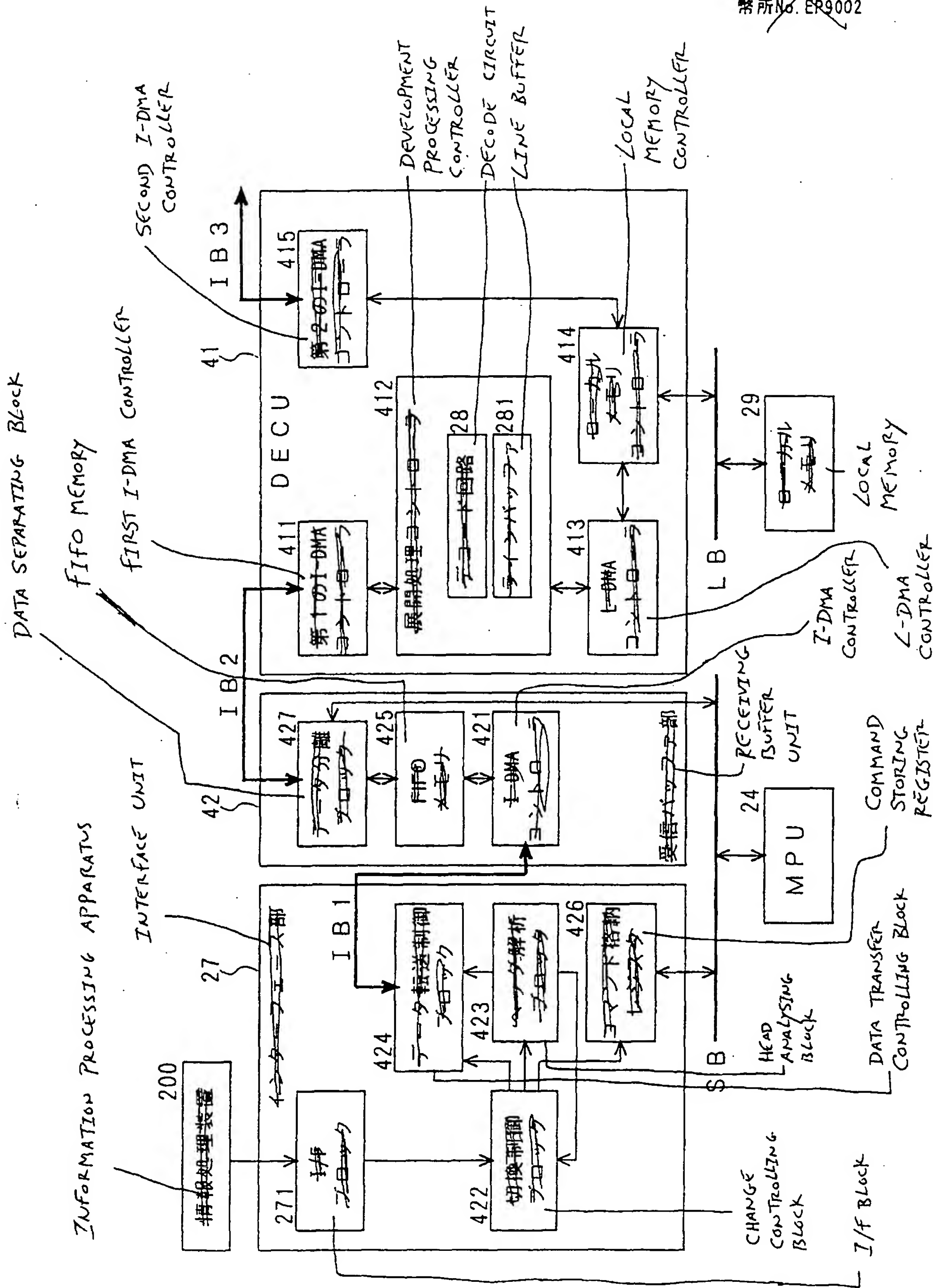
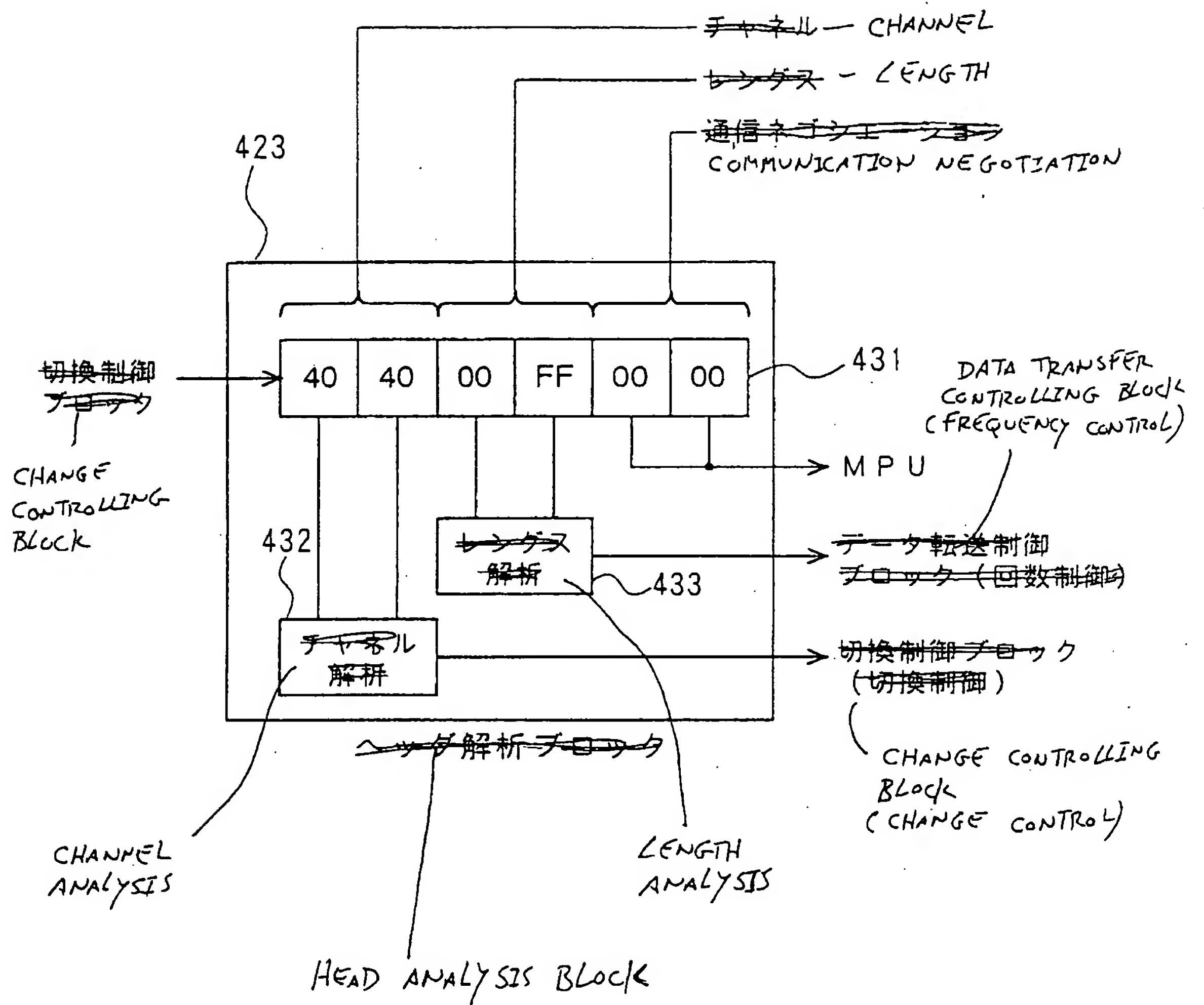


FIG. 7



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OPERATION CONDITION

MAIN MEMORY SIDE: STARTING ADDRESS OF RUN LENGTH DATA IS AN EVEN ADDRESS

LOCAL MEMORY SIDE: STARTING ADDRESS OF IMAGE DATA IS AN EVEN ADDRESS

NUMBER OF BYTES IN 1 LINE: 16 BYTES

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FIG. 9

動作条件

メインメモリ側: ランレングスデータの開始アドレス 偶数アドレス

ローカルメモリ側: イメージデータの開始アドレス 偶数アドレス

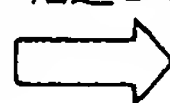
1ラインバイト数: 16バイト

MAIN MEMORY

FE 01	TRANSFER S1	FE 01															
03 02	FACE A	A面	01 01	01													
78 55	FACE B	B面															
44 FB	TRANSFER S2	03 02															
FF FE	TRANSFER S2	A面	01 01	01 02													
11 06	FACE A	A面															
66 12	TRANSFER S3	78 55															
77 45	TRANSFER S3	A面	01 01	01 02	78 55												
89 10	FACE A	A面															
55 FB	TRANSFER S4	44 FB															
10 FA	TRANSFER S4	A面	01 01	01 02	78 55	44											
20 08	FACE A	A面															
12 13	TRANSFER S5	FF FE															
14 15	TRANSFER S5	A面	01 01	01 02	78 55	44 FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
16 17	FACE A	A面															
18 19	TRANSFER S6	11 06															
20 FD	TRANSFER S6	A面	01 01	01 02	78 55	44 FF	FF	FF	FF	FF	FF	FF	11	11	11	11	11
11 02	FACE A	A面															
98 B0	TRANSFER S7	66 12															
F2 FC	TRANSFER S7	A面															
AB 03	FACE A	A面															
FF FE	TRANSFER S8	77 45															
FC FD	TRANSFER S8	A面															
FE FF	FACE A	A面															
	TRANSFER S9	89 10															
	FACE A	A面															
	FACE B	B面	66 12	77 45	89 10												
	TRANSFER S10	55 FB															
	FACE A	A面															
	FACE B	B面	66 12	77 45	89 10	55											
	TRANSFER S11	10 FA															
	FACE A	A面															
	FACE B	B面	66 12	77 45	89 10	55 10	10 10	10 10	10 10	10 10							
	TRANSFER S12	20 08															
	FACE A	A面	20 20	20 20													
	FACE B	B面	66 12	77 45	89 10	55 10	10 10	10 10	10 10	10 20	20 20						

TRANSFER D1

転送D1



TRANSFER D2

転送D2

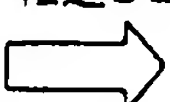
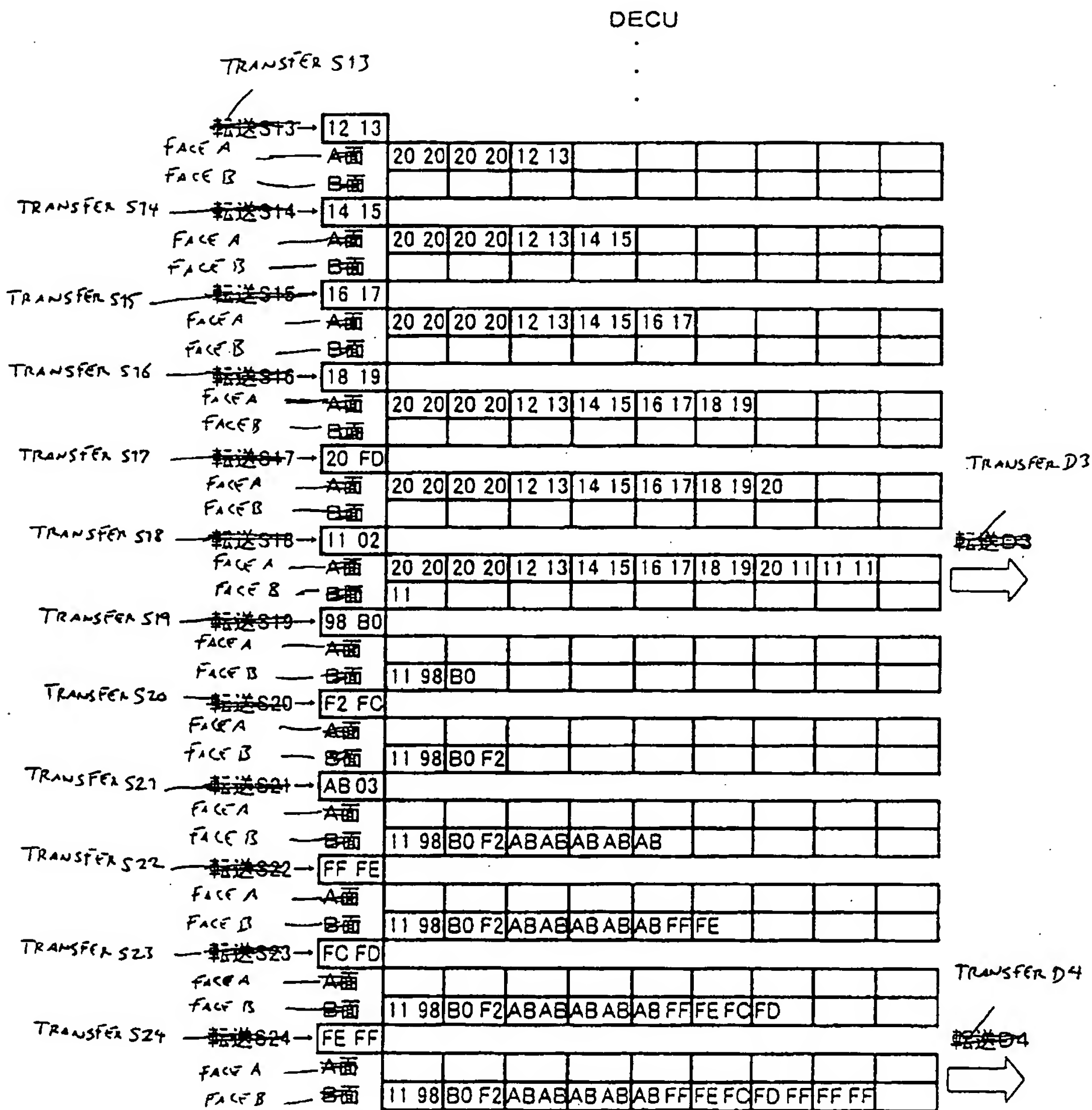


FIG. 10



SETTING CONDITION

NO VERTICAL LINE REARRANGEMENT

TOTAL NUMBER OF DEVELOPED BYTES: 64 BYTES (16X4)

NUMBER OF BYTES IN 1 LINE: 16 BYTES

NUMBER OF DEVELOPED LINES: 4 LINES

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FIG. 11

設定条件

ライン縦並び変換なし

総展開バイト数: 64バイト (16X4)

1ラインバイト数: 16バイト

展開ライン数: 4ライン

~~ローカルメモリ~~ LOCAL MEMORY

FIG. 11A

(a) D1 →

01 01	01 02	78 55	44 FF
FF FF	FF FF	FF 11	11 11
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00

FIG. 11B

(b) D2 →

01 01	01 02	78 55	44 FF
FF FF	FF FF	FF 11	11 11
66 12	77 45	89 10	55 10
10 10	10 10	10 20	20 20
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00

FIG. 11C

(c) D3 →

01 01	01 02	78 55	44 FF
FF FF	FF FF	FF 11	11 11
66 12	77 45	89 10	55 10
10 10	10 10	10 20	20 20
20 20	20 20	12 13	14 15
16 17	18 19	20 11	11 11
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00

FIG. 11D

(d) D4 →

01 01	01 02	78 55	44 FF
FF FF	FF FF	FF 11	11 11
66 12	77 45	89 10	55 10
10 10	10 10	10 20	20 20
20 20	20 20	12 13	14 15
16 17	18 19	20 11	11 11
11 98	B0 F2	ABAE	ABAE
AB FF	FE FC	FD FF	FF FF

FIG. 12

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